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1 Dependency graph scheduling in a volumetric ray tracing architectur
S. Frank, A. Kaufman
September 2002 HWWS '02: Proceedings of the ACM SIGGRAPH/EUROGRA conference on Graphics hardware
Publisher: Eurographics Association
Full text available: Pdf (220.01 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 23, Citation (

We propose a volumetric ray tracing PCI board which uses FPGA compo chip memory. In a multiboard system a super volume (i.e., one that is l board memory) can be either distributed or shared. In a single board sy be ...

Keywords: inter-frame coherence, load balancing, super volume, volur tracing

2 Speculative hedge: regulating compile-time speculation against profi
Brian L. Deitrich, Wen-mei W. Hwu
December 1996 MICRO 29: Proceedings of the 29th annual ACM/IEEE inter symposium on Microarchitecture
Publisher: IEEE Computer Society
Full text available: Pdf (1.33 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 15, Citation (

Path-oriented scheduling methods, such as trace scheduling and hyperb use speculation to extract instruction-level parallelism from control-inte These methods predict important execution paths in the current schedu

3 Scheduling over nonstationary wireless channels with finite rate sets
Matthew Andrews, Lisa Zhang
October 2006 IEEE/ACM Transactions on Networking (TON) , Volume 14
Publisher: IEEE Press
Full text available: Pdf (496.93 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 52, Citation (

We consider a wireless basestation transmitting high-speed data to mul

users in a cell. The channel conditions between the basestation and the varying and user-dependent. Our objective is to design a scheduler that

Keywords: nonstationary channel rates, wireless scheduling

4 A framework for scheduling multi-rate circuit simulation

A. P.-C. Ng, V. Visvanathan

June 1989 DAC '89: Proceedings of the 26th ACM/IEEE conference on Desi

Publisher: ACM

Full text available: Pdf (745.68 KB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 13, Citation (

This paper presents a theoretical framework for scheduling of subcircuit multirate simulation environment. We show that event-driven simulatio trace, and latency are subsumed ...

5 Social and psychological factors influencing the design of office com systems

Susan F. Ehrlich

April 1987 CHI '87: Proceedings of the SIGCHI/GI conference on Human fa computing systems and graphics interface

Publisher: ACM

Full text available: Pdf (619.96 KB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 52, Citation

Office automation is used by groups of people with complex communica help them reach business goals such as scheduling, tracking, reviewing, Effective individual and group decisions are heavily dependent on comm

Keywords: communication, electronic mail, office systems, sociology, v

Also published in:

May 1986 SIGCHI Bulletin Volume 17 Issue SI

April 1987 SIGCHI Bulletin Volume 18 Issue 4

6 An algorithm for global compaction of horizontal microprograms

S. Upendra Rao, A. K. Majundar

July 1986 SIGMICRO Newsletter , Volume 17 Issue 2

Publisher: ACM

Full text available: Pdf (506.43 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 2, Citation C

Compacting microoperations of a microprogram into horizontal microins an efficient global compaction algorithm. This paper describes a global c algorithm which is more practical than some of the existing techniques l

7 Global microcode compaction under timing constraints

B. Su, J. Wang, J. Xia

January 1988 MICRO 21: Proceedings of the 21st annual workshop on Micr and microarchitecture
Publisher: IEEE Computer Society Press
Full text available: Pdf (265.18 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 3, Citation C

Existing global microcode compaction algorithms have all been based or that the parallelism exploitation is constrained only by data dependency limitation. However, the timing constraint also has great impact upon m

8 Scheduling strategies for optimistic parallel execution of irregular pro
Milind Kulkarni, Patrick Carribault, Keshav Pingali, Ganesh Ramanarayanan Kavita Bala, L. Paul Chew
June 1998 SPAA '08: Proceedings of the twentieth annual symposium on P algorithms and architectures
Publisher: ACM
Full text available: Pdf (394.96 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 65, Citation

Recent application studies have shown that many irregular applications generalized data parallelism that manifests itself as iterative computatic worklists of different kinds. In general, there are complex dependencies iterations. ...

Keywords: irregular programs, optimistic parallelism, scheduling

9 Accurate static branch prediction by value range propagation
Jason R. C. Patterson
June 1995 PLDI '95: Proceedings of the ACM SIGPLAN 1995 conference on language design and implementation
Publisher: ACM
Full text available: Pdf (1.26 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 64, Citation

The ability to predict at compile time the likelihood of a particular branc provides valuable information for several optimizations, including global scheduling, code layout, function inlining, interprocedural register alloca

Also published in:
June 1995 SIGPLAN Notices Volume 30 Issue 6

10 Code scheduling and register allocation in large basic blocks
J. R. Goodman, W.-C. Hsu
June 1988 ICS '88: Proceedings of the 2nd international conference on Sup
Publisher: ACM
Full text available: Pdf (1.20 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 67, Citation (

We discuss the issues about the interdependency between code schedul allocation. We present two methods as solutions: (1) an integrated code technique; and (2) a DAG-driven register allocator. The integrated code

11 A debugger for flow graph based parallel applications

Ali Al-Shabibi, Sebastian Gerlach, Roger D. Hersch, Basile Schaeli

July 2007 PADTAD '07: Proceedings of the 2007 ACM workshop on Paralle systems: testing and debugging

Publisher: ACM

Full text available: Pdf (344.87 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 57, Citation C

Flow graphs provide an explicit description of the parallelization of an ap mapping vertices onto serial computations and edges onto message trai present the design and implementation of a debugger for the flow graph Dynamic ...

Keywords: flow graph based debugging, flow graph based parallel app message race detection, message reordering, parallel schedules

12 Synchronization and recovery of actions

J. E. Allchin, M. S. McKendry

January 1985 SIGOPS Operating Systems Review , Volume 19 Issue 1

Publisher: ACM

Full text available: Pdf (1.26 MB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 8, Citation C

We introduce an approach to robust computation in distributed systems is the foundation for reliability in the Clouds decentralized operating sy on atomic actions operating on instances of abstract data types (objects

13 The accuracy of trace-driven simulations of multiprocessors

Stephen R. Goldschmidt, John L. Hennessy

June 1993 SIGMETRICS '93: Proceedings of the 1993 ACM SIGMETRICS c Measurement and modeling of computer systems

Publisher: ACM

Full text available: Pdf (1.26 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 68, Citation C

In trace-driven simulation, traces generated for one set of system chara used to simulate a system with different characteristics. However, the e a multiprocessor workload may depend on the order of events occurring

Also published in:

June 1993 SIGMETRICS Performance Evaluation Review Volume 21 Issue 1

14 Efficient superscalar performance through boosting

Michael D. Smith, Mark Horowitz, Monica S. Lam

September 1992 ASPLOS-V: Proceedings of the fifth international conferenc Architectural support for programming languages and oper

Publisher: ACM

Full text available: Pdf (1.63 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 25, Citation (

The foremost goal of superscalar processor design is to increase perforn the exploitation of instruction-level parallelism (ILP). Previous studies h speculative execution is required for high instruction per cycle (IPC) rat

Also published in:
September 1992 SIGPLAN Notices Volume 27 Issue 9

15 Memory access scheduling

Scott Rixner, William J. Dally, Ujval J. Kapasi, Peter Mattson, John D. Ower
June 2000 ISCA '00: Proceedings of the 27th annual international symposi architecture

Publisher: ACM

Full text available: Pdf (181.84 KB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 124, Citatio

The bandwidth and latency of a memory system are strongly dependent in which accesses interact with the "3-D" structure of banks, rows, and characteristic of contemporary DRAM chips. There is nearly an order ...

Also published in:
May 2000 SIGARCH Computer Architecture News Volume 28 Issue 2

16 Synchronization and recovery of actions

J. E. Allchin, M. S. McKendry
August 1983 PODC '83: Proceedings of the second annual ACM symposium distributed computing

Publisher: ACM

Full text available: Pdf (1.29 MB) Additional Information: full citation, abstract, referen terms

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 14, Citation (

We introduce an approach to robust computation in distributed systems is the foundation for reliability in the Clouds decentralized operating sys on atomic actions operating on instances of abstract data types ...

17 SuperMatrix: a multithreaded runtime scheduling system for algorith

Ernie Chan, Field G. Van Zee, Paolo Bientinesi, Enrique S. Quintana-Orti, G Quintana-Orti, Robert van de Geijn
February 2008 PPoPP '08: Proceedings of the 13th ACM SIGPLAN Symposi and practice of parallel programming

Publisher: ACM

Full text available: Pdf (357.05 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 168, Citatio

This paper describes SuperMatrix, a runtime system that parallelizes m for SMP and/or multi-core architectures. We use this system to demons

described at a high level of abstraction can achieve high performance o
architectures ...

Keywords: algorithms-by-blocks, dependency analysis, dynamic sched
order execution

18 Scheduling over a time-varying user-dependent channel with applica
speed wireless data
Matthew Andrews, Lisa Zhang
September 2005 Journal of the ACM (JACM) , Volume 52 Issue 5
Publisher: ACM
Full text available: Pdf (327.32 KB) Additional Information: full citation, abstract, referen
terms

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 92, Citation (

In a wireless network, a basestation transmits data to mobiles at *time-*
dependent rates due to the ever changing nature of the communication
this article, we consider a wireless system in which the channel conditio

Keywords: Scheduling, stability, time-varying, user-depedent, wireless

19 Optimizing refresh of a set of materialized views
Nathan Folkert, Abhinav Gupta, Andrew Witkowski, Sankar Subramanian, S
Bellamkonda, Shrikanth Shankar, Tolga Bozkaya, Lei Sheng
August 2005 VLDB '05: Proceedings of the 31st international conference or
bases
Publisher: VLDB Endowment
Full text available: Pdf (381.22 KB) Additional Information: full citation, abstract, referen

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 60, Citation (

In many data warehousing environments, it is common to have materia
(MVs) at different levels of aggregation of one or more dimensions. The
this is relational OLAP environments, where, for performance reasons, r
levels ...

20 WISQ: a restartable architecture using queues
A. R. Pleszkun, J. R. Goodman, W. C. Hsu, R. T. Joersz, G. Bier, P. Woest,
June 1987 ISCA '87: Proceedings of the 14th annual international symposi
architecture
Publisher: ACM
Full text available: Pdf (1.14 MB) Additional Information: full citation, abstract, referen
terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 22, Citation (

In this paper, the WISQ architecture is described. This architecture is de
achieve high performance by exploiting new compiler technology and us
segmented pipeline. By having a highly segmented pipeline, a very-high
can ...

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